

Fig. 1

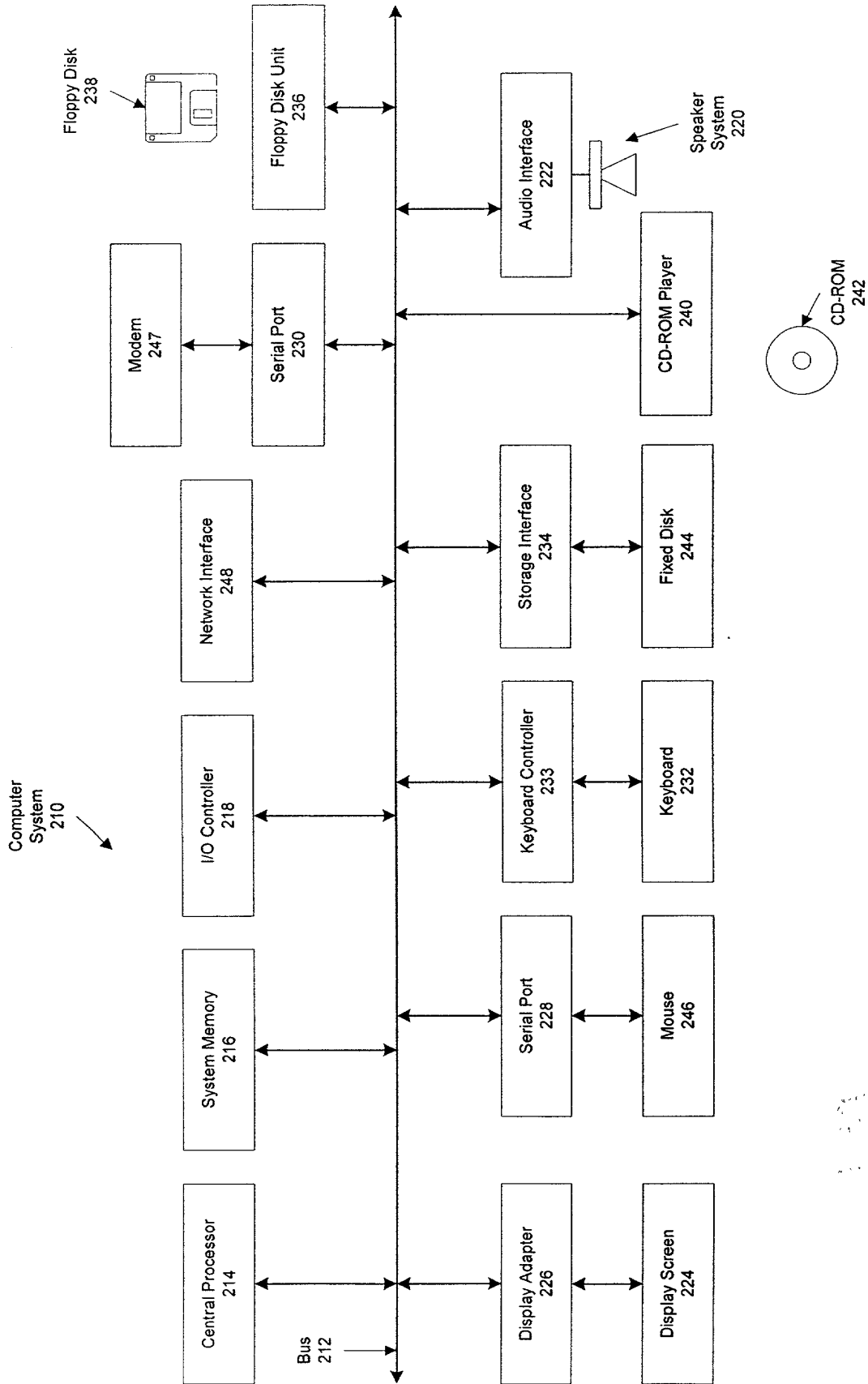


Fig. 2



Fig. 4

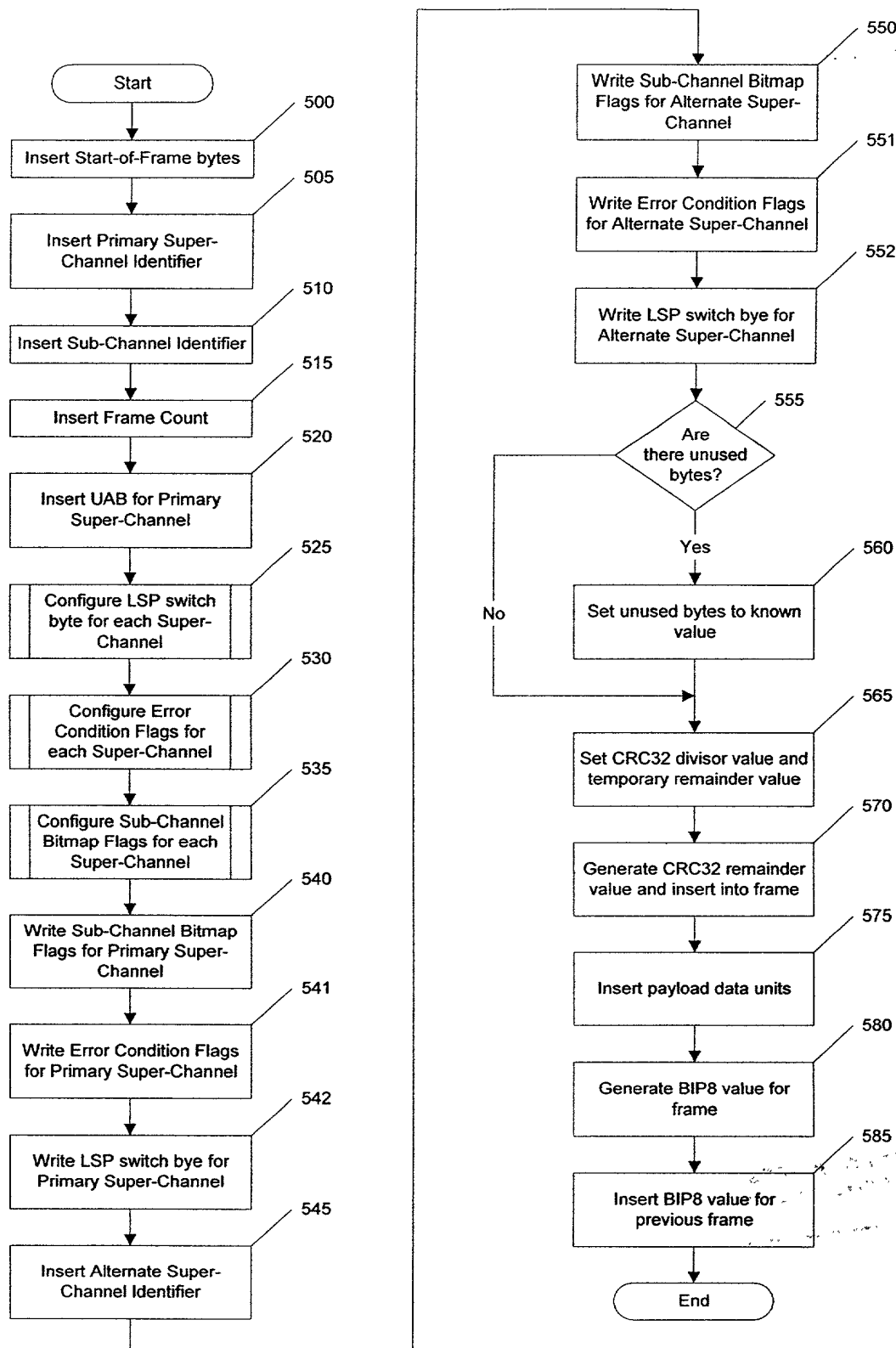


Fig. 5

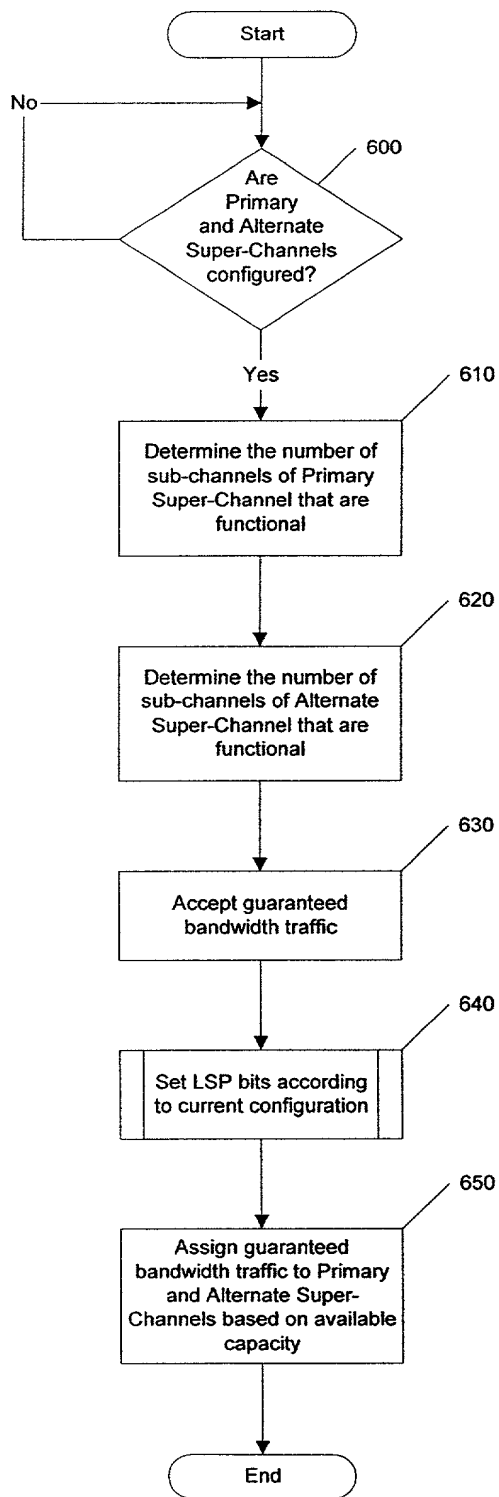


Fig. 6

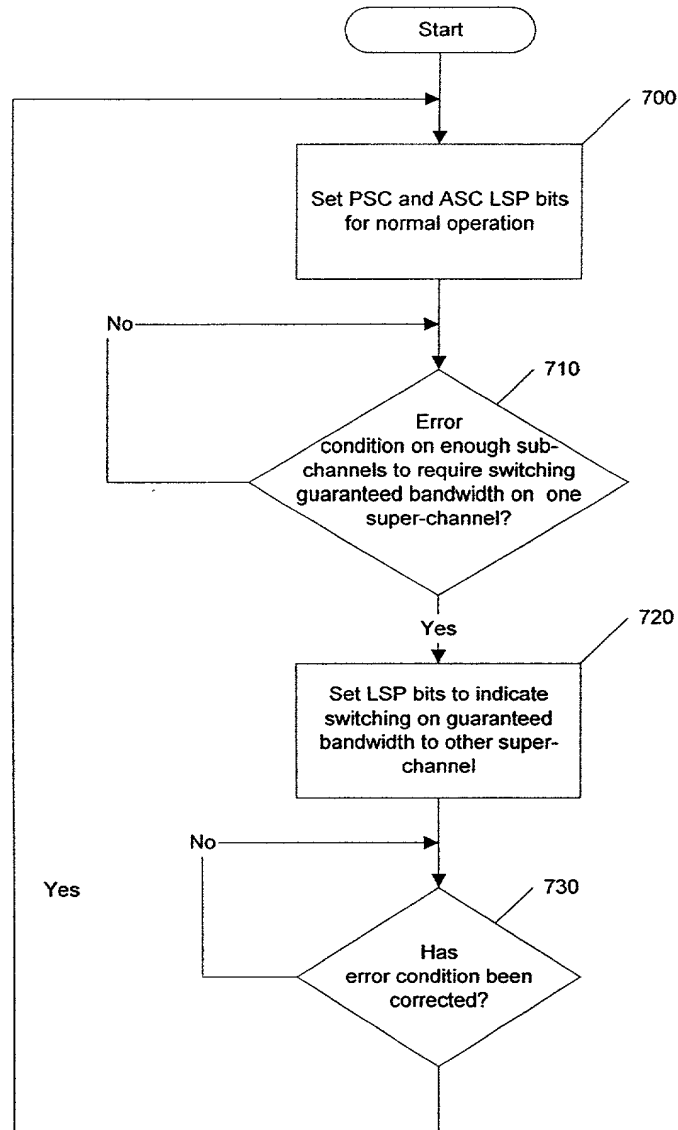


Fig. 7

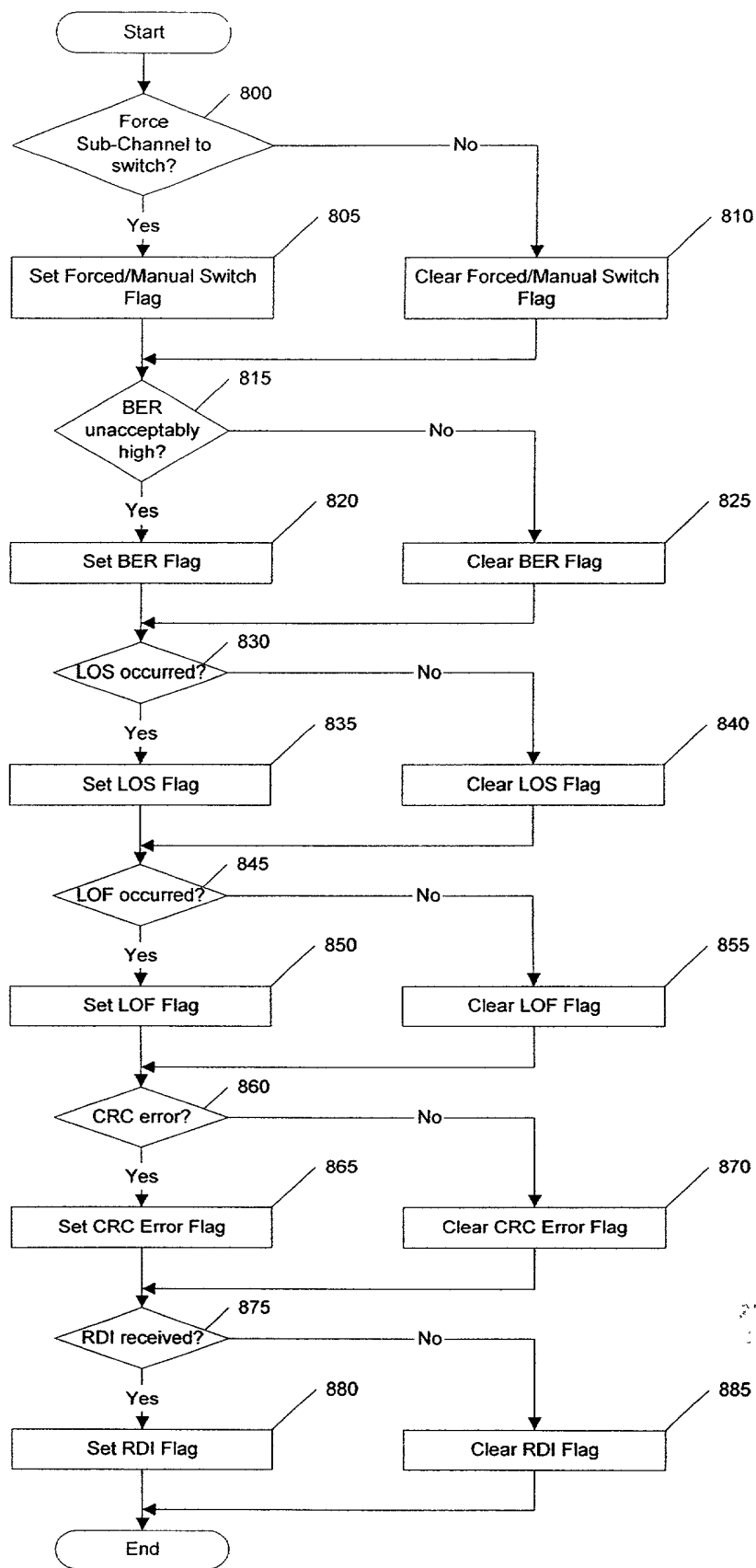


Fig. 8

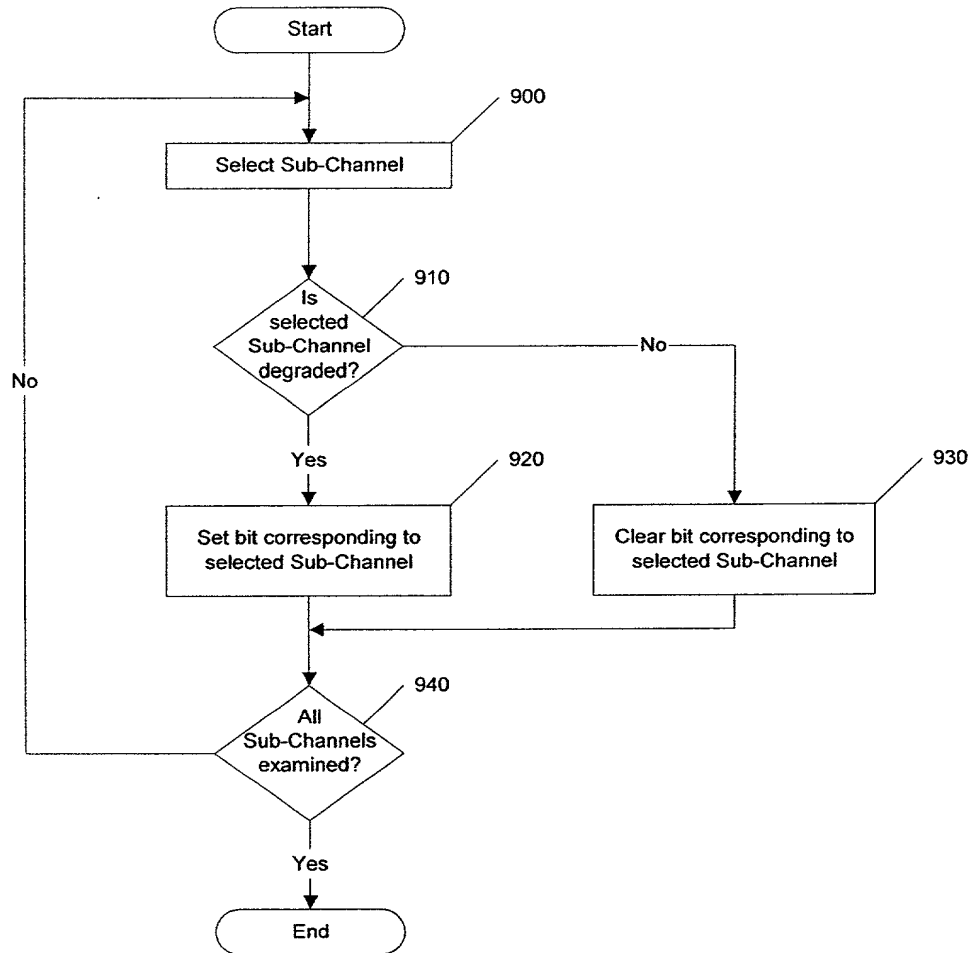


Fig. 9

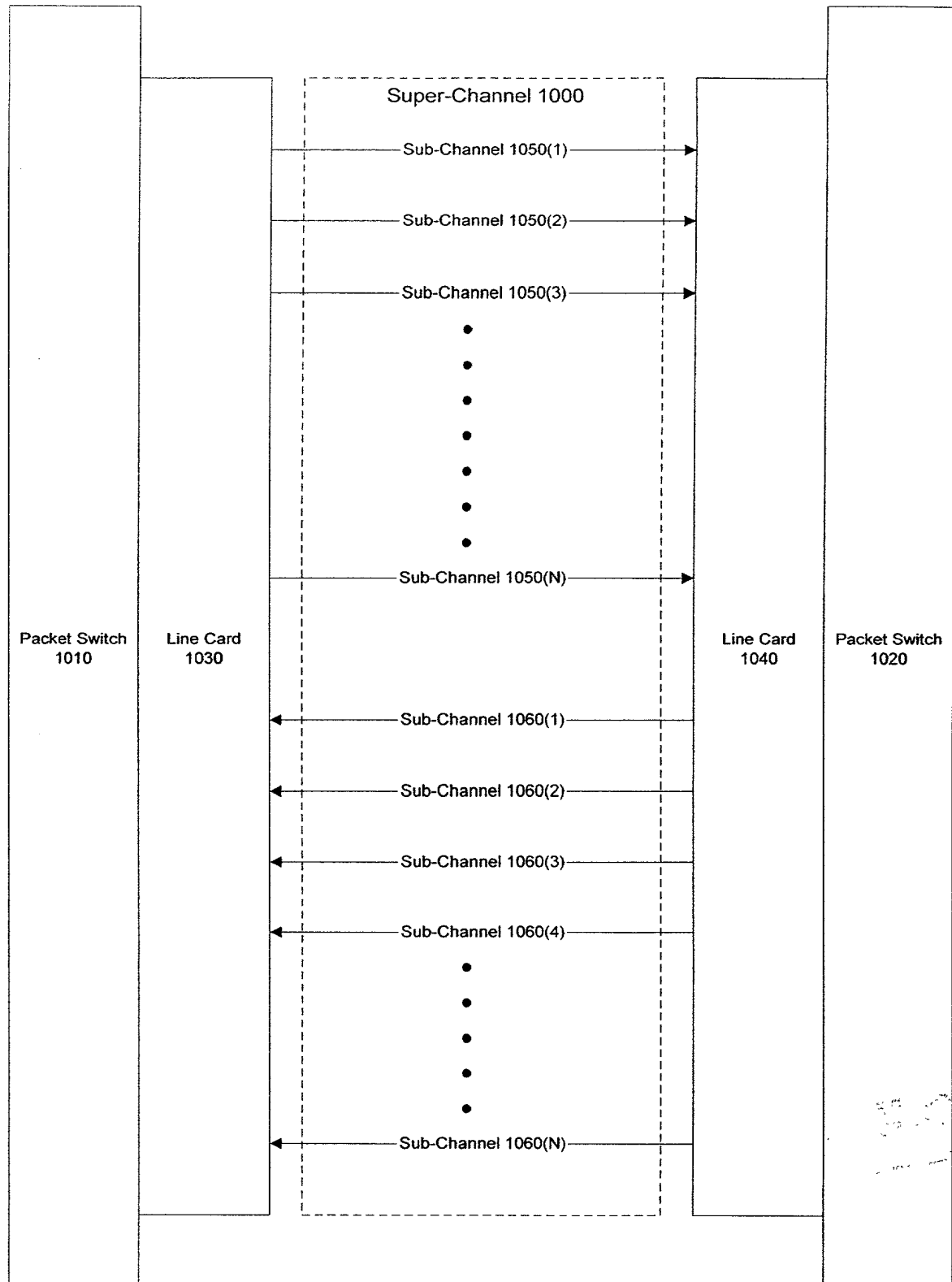


Fig. 10

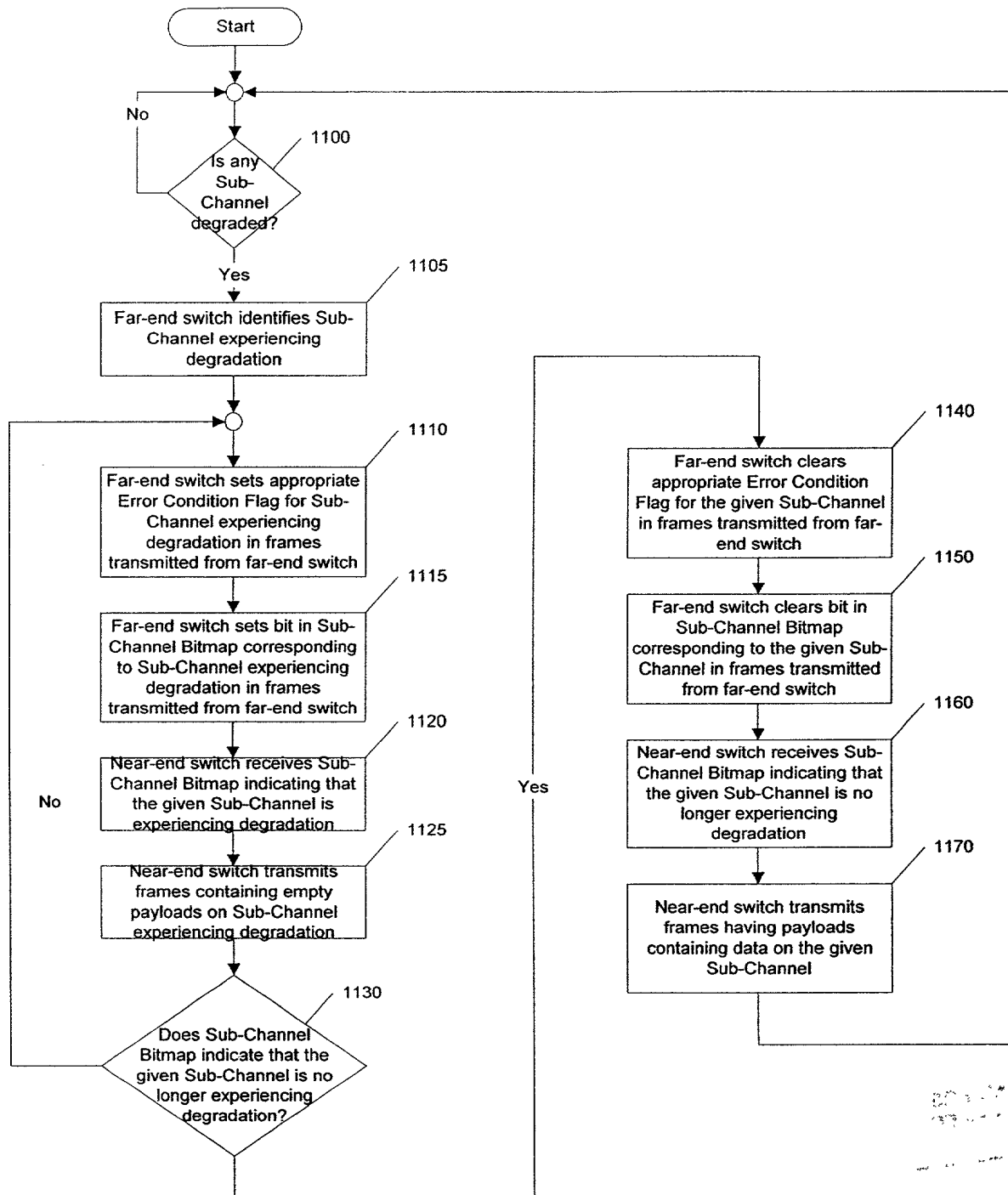


Fig. 11

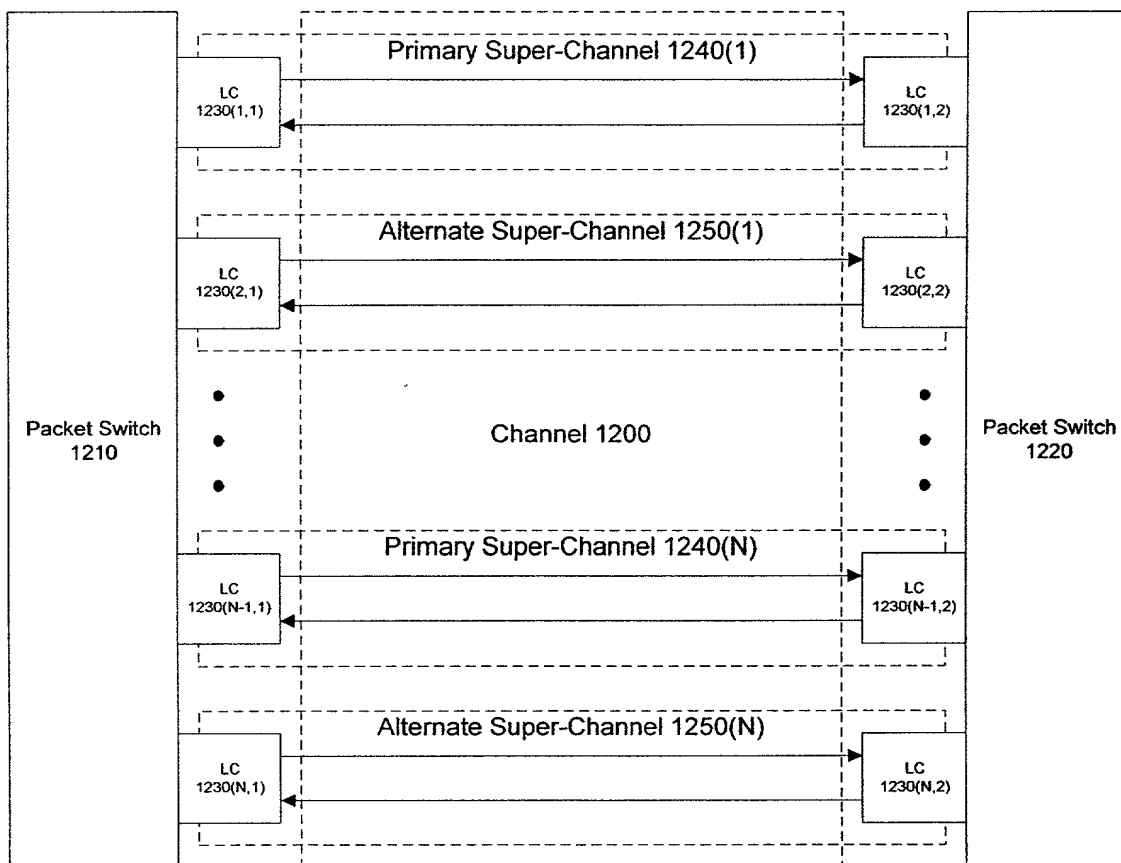


Fig. 12

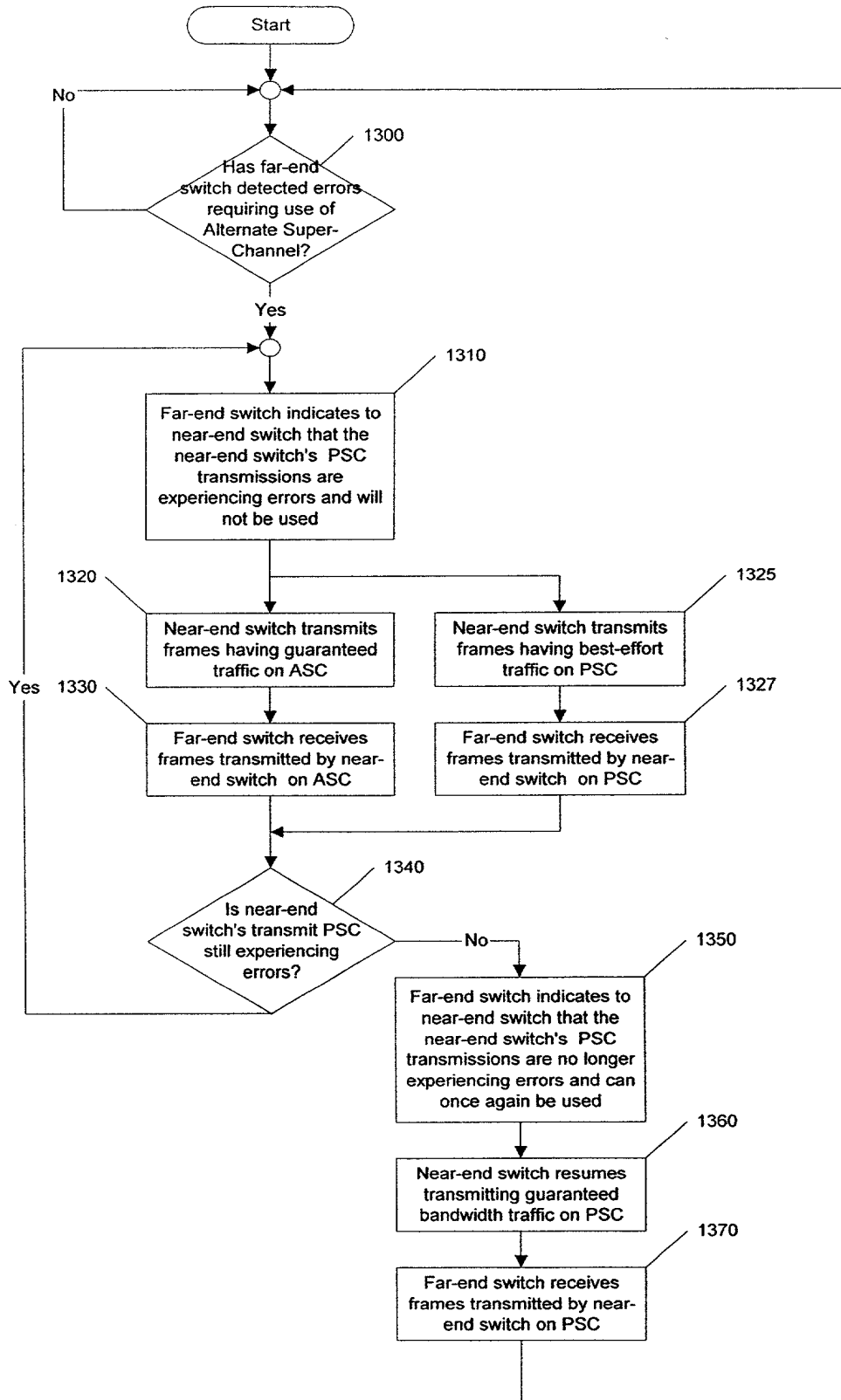


Fig. 13

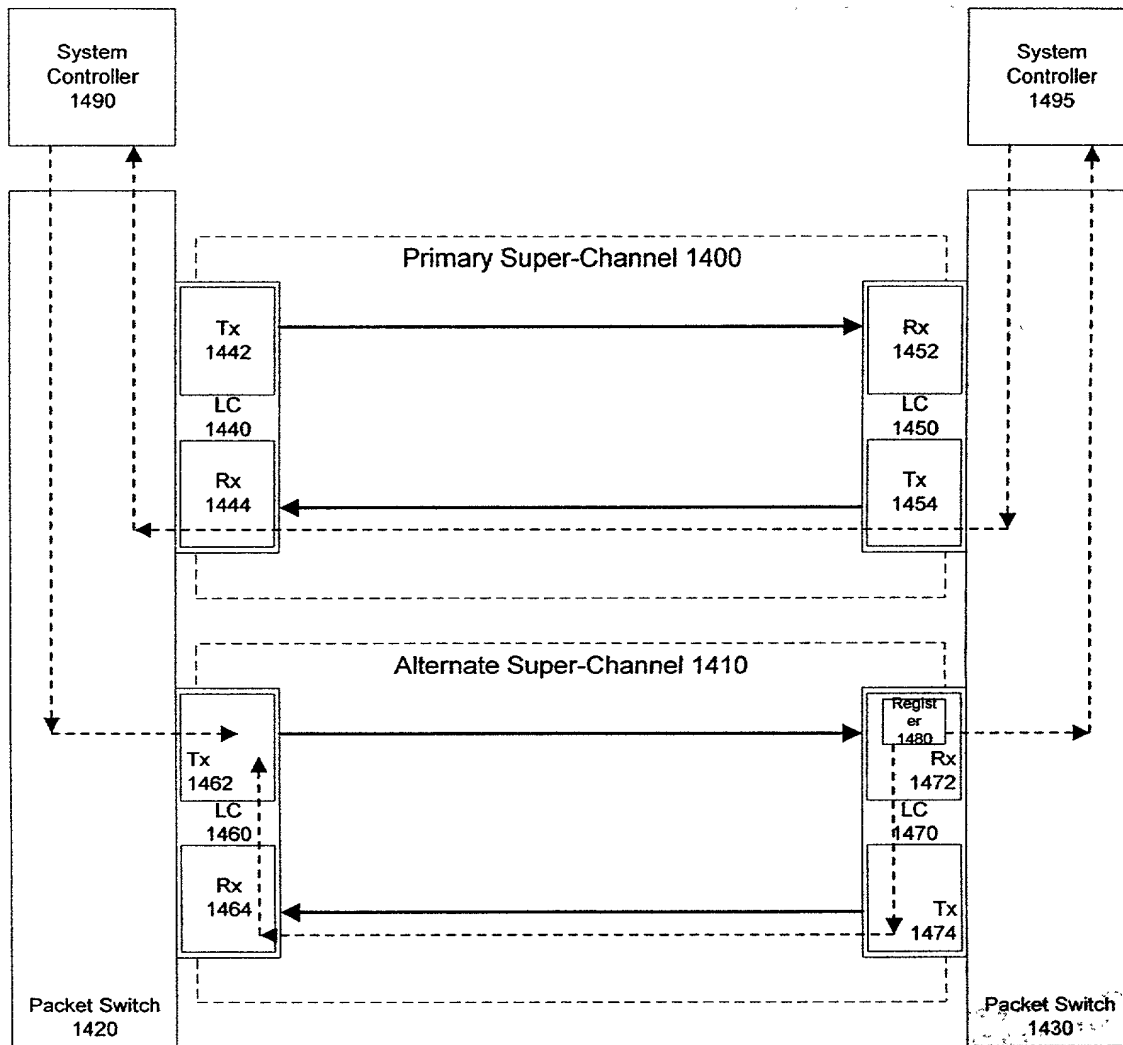


Fig. 14

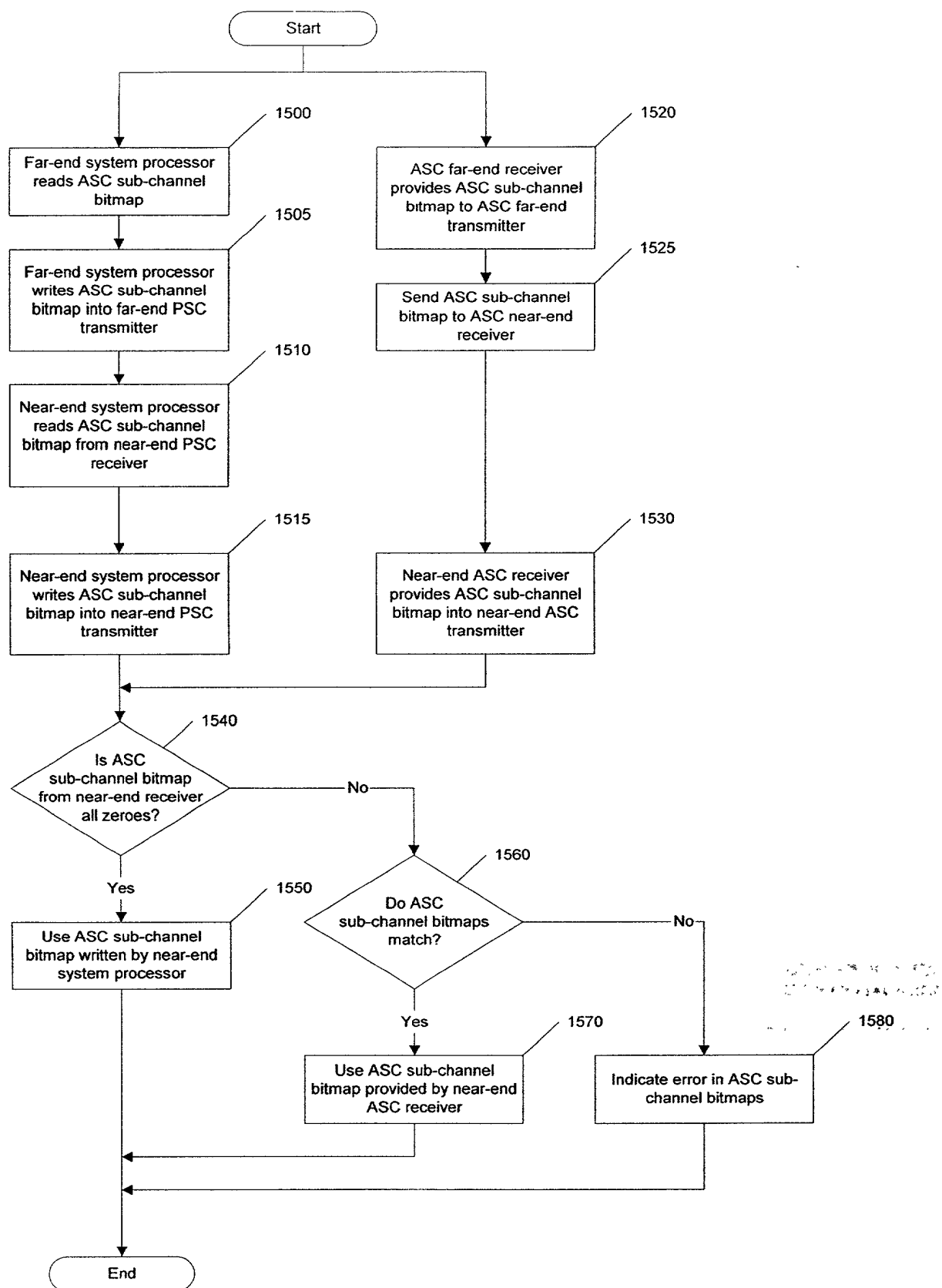


Fig. 15

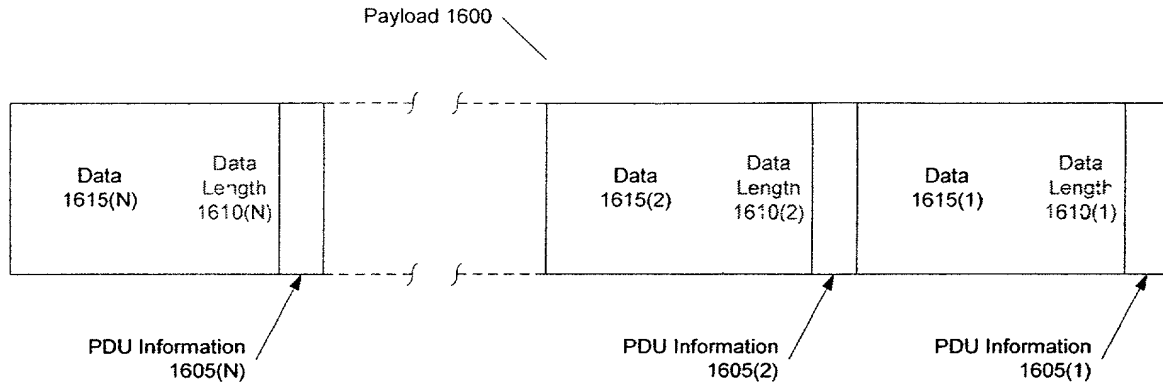


Fig. 16A

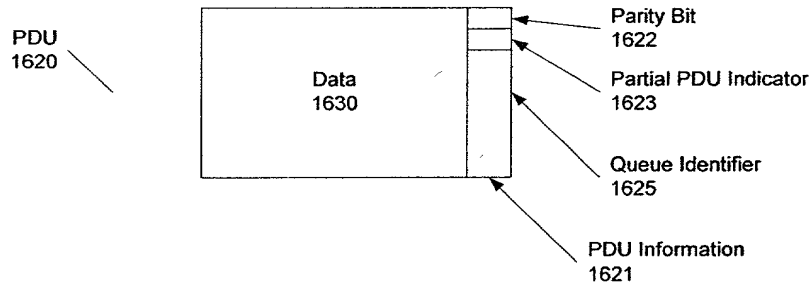


Fig. 16B

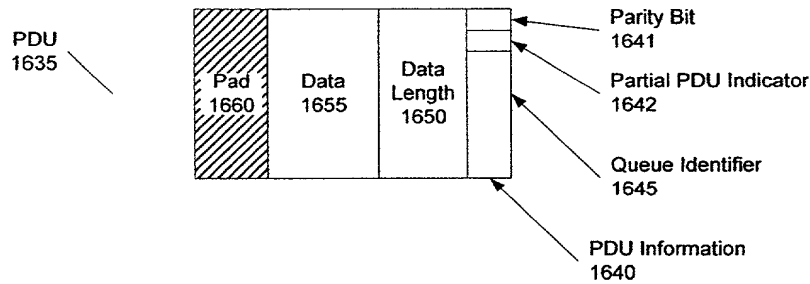


Fig. 16C

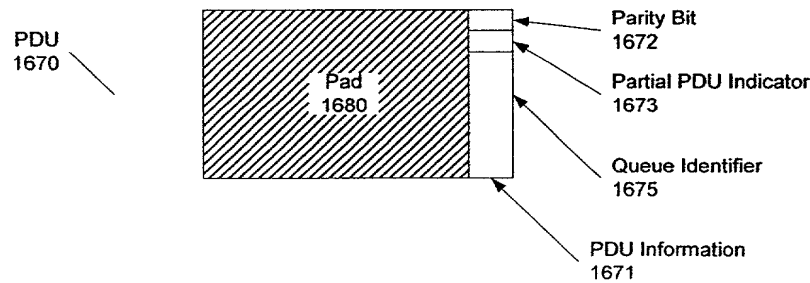


Fig. 16D

2007/07/20 10:00

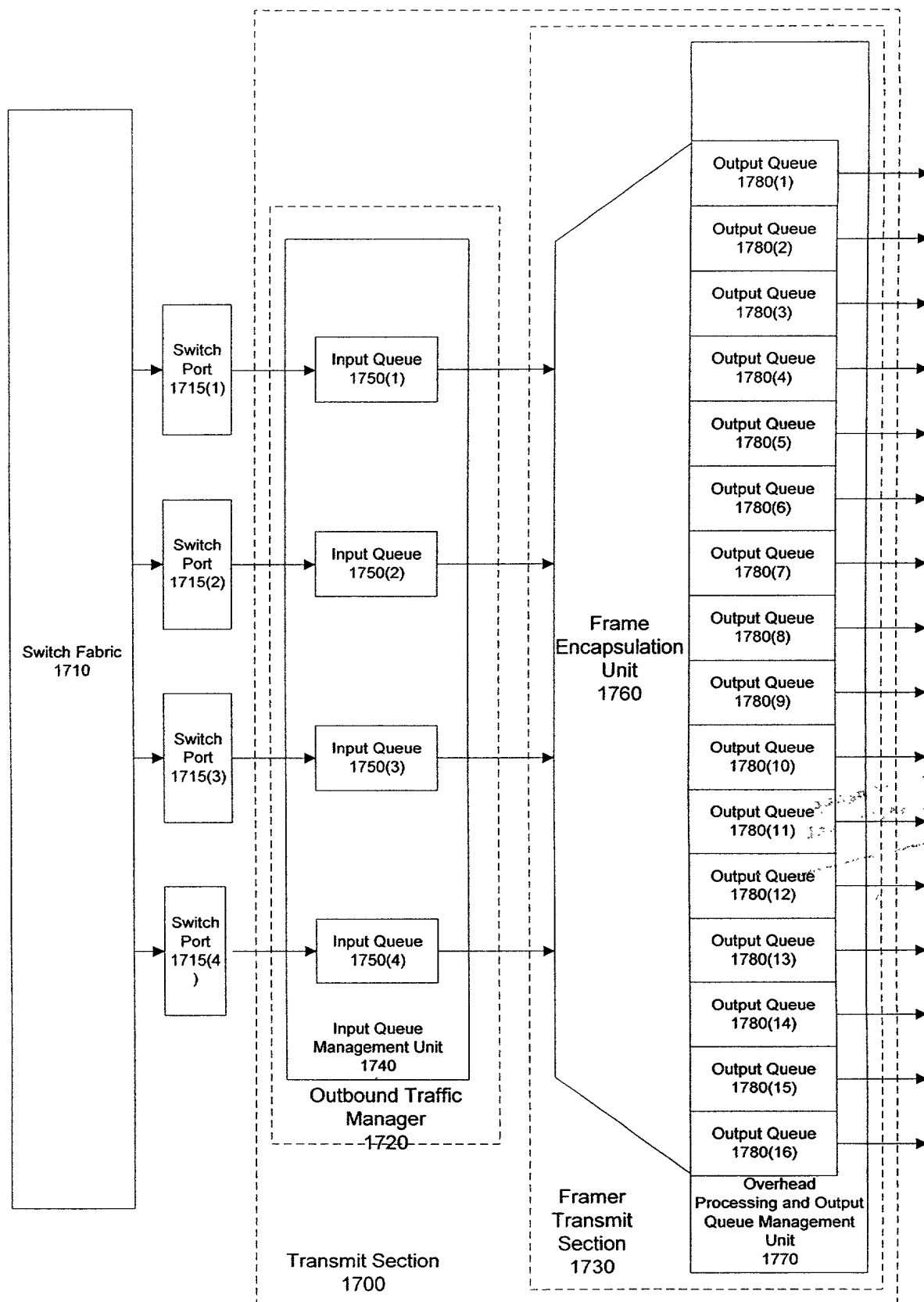


Fig. 17

Transmit Section
1700

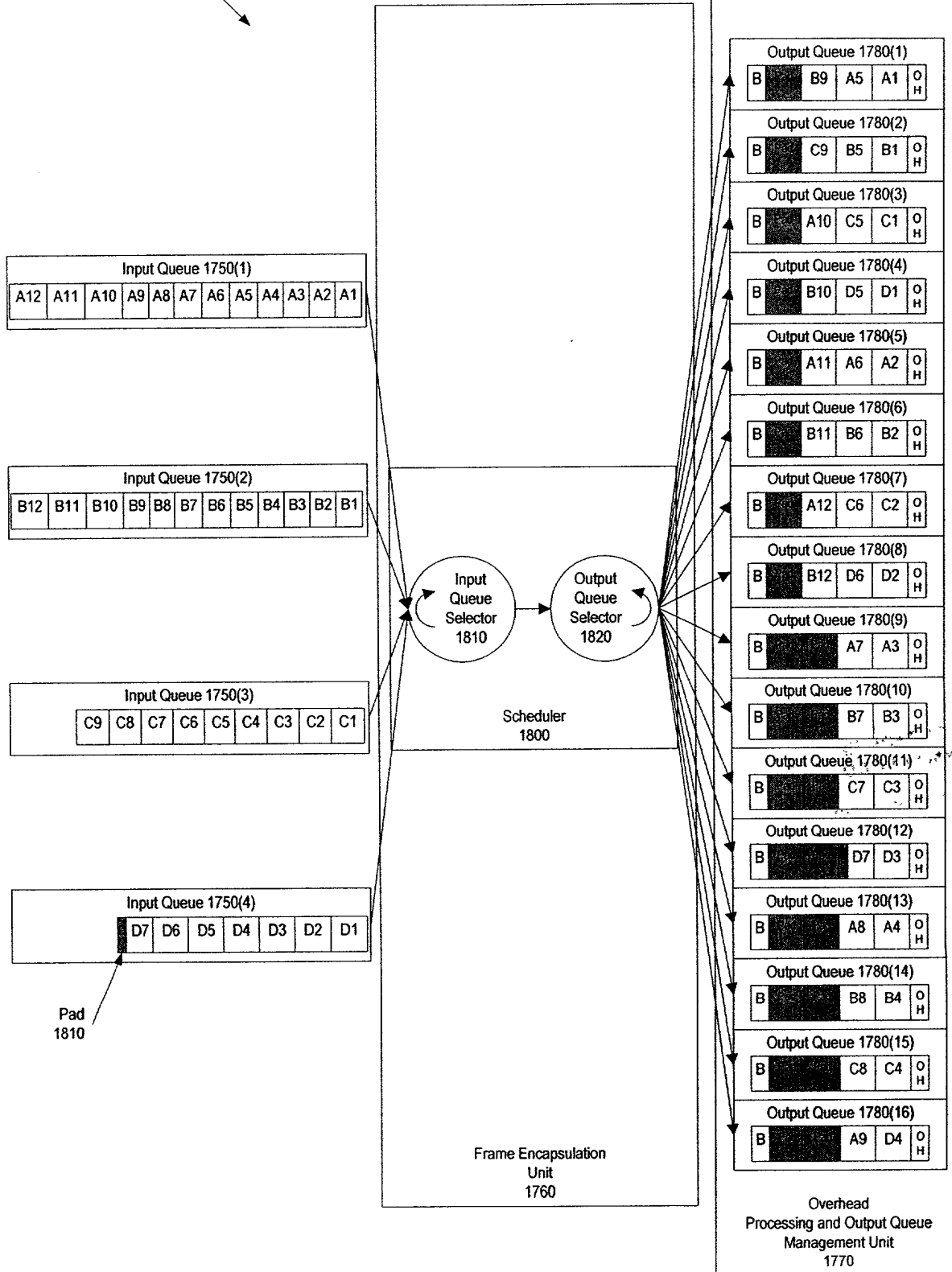


Fig. 18

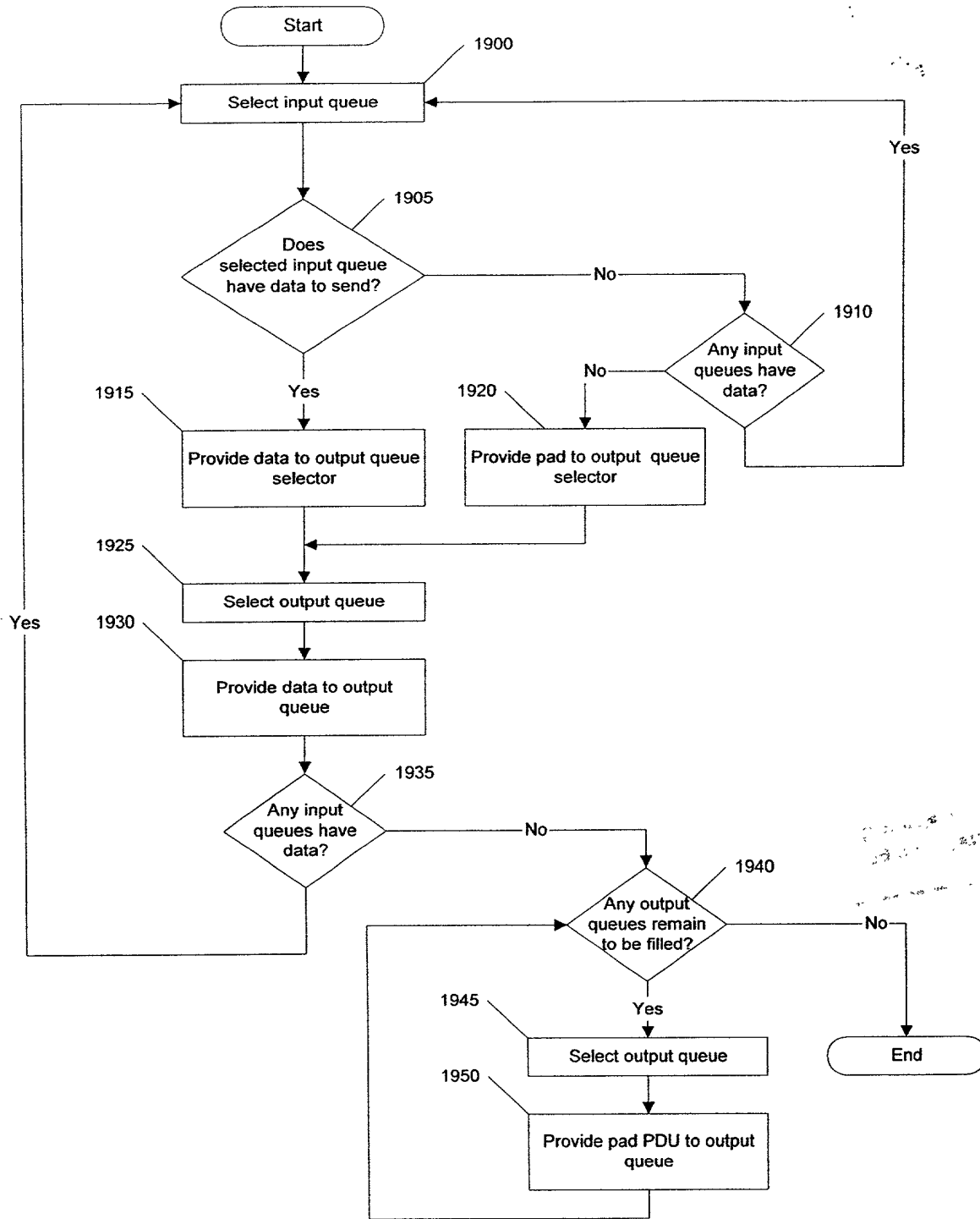


Fig. 19



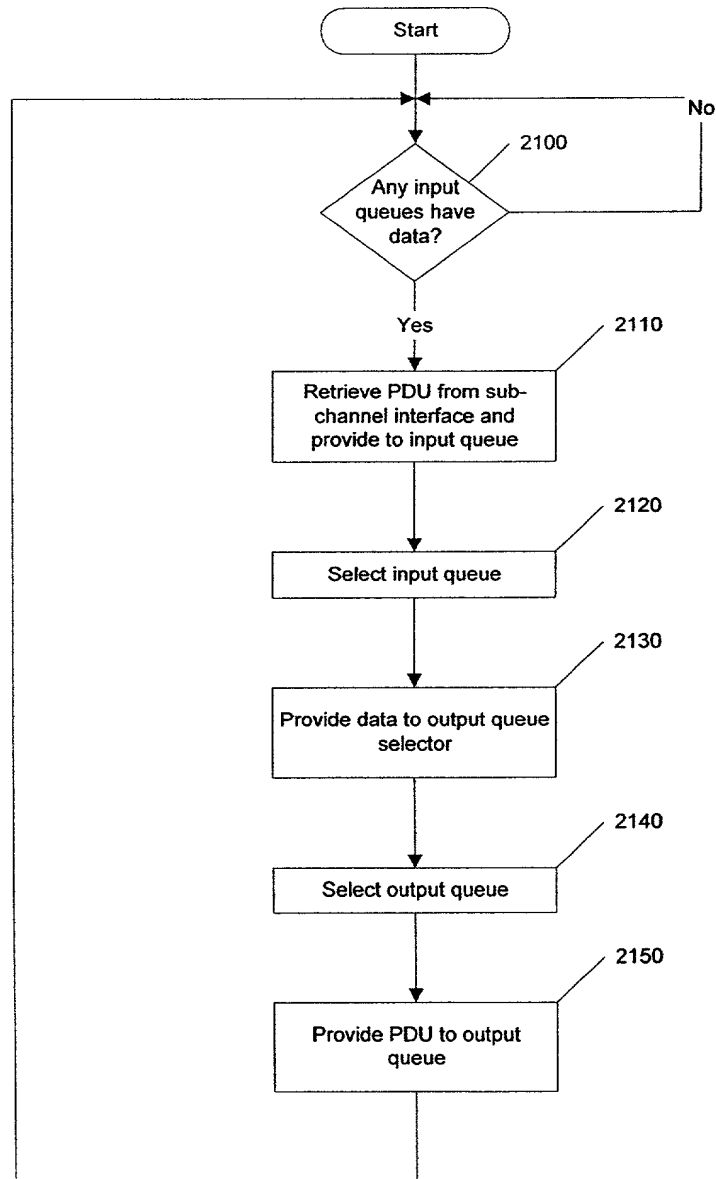


Fig. 21

Sub-Channel Frame Synchronization
State Machine
2200

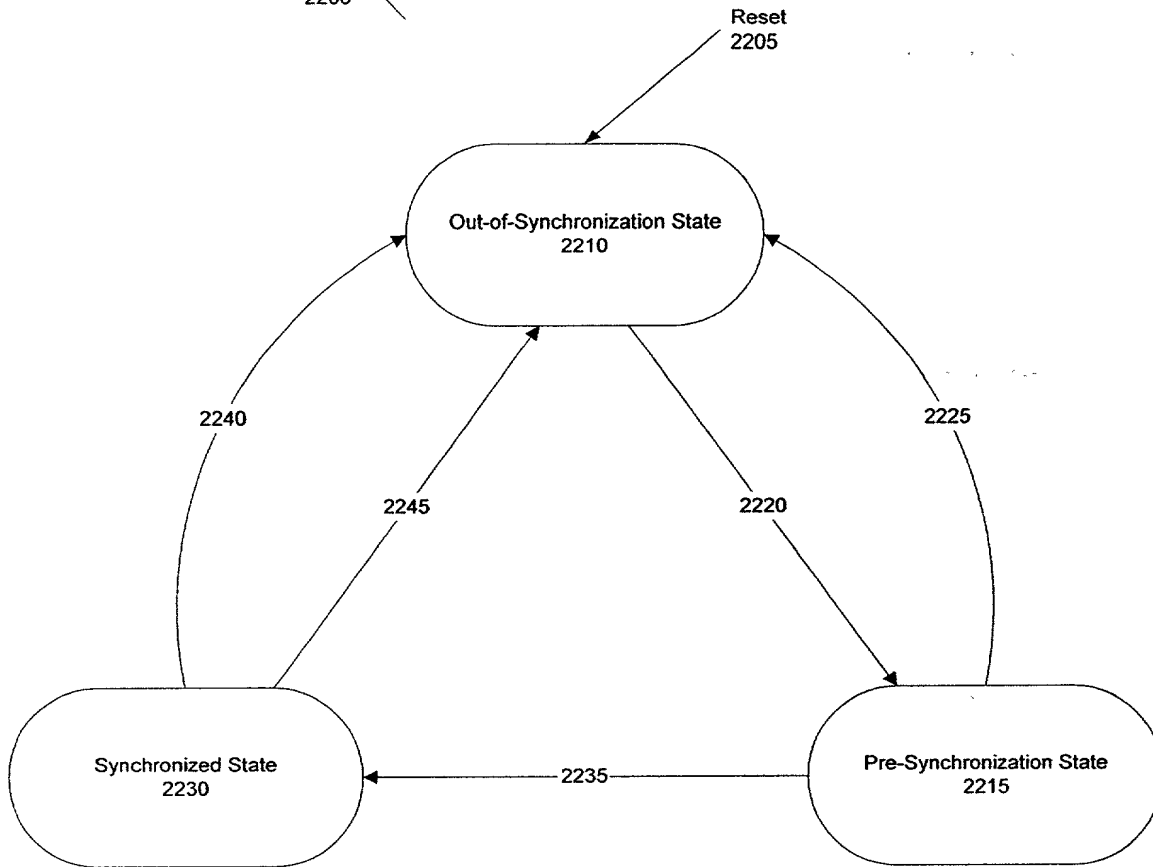


Fig. 22

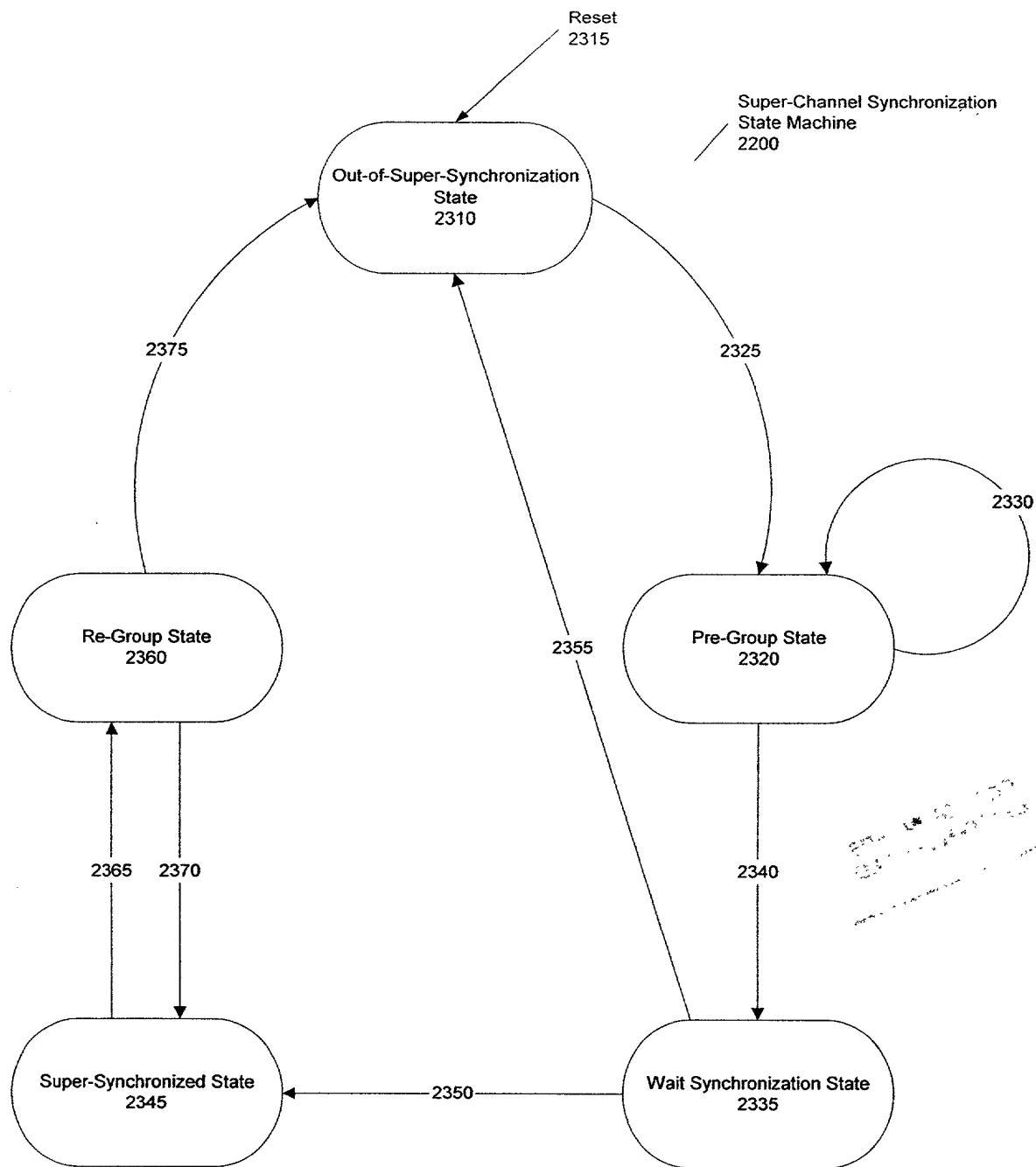


Fig. 23